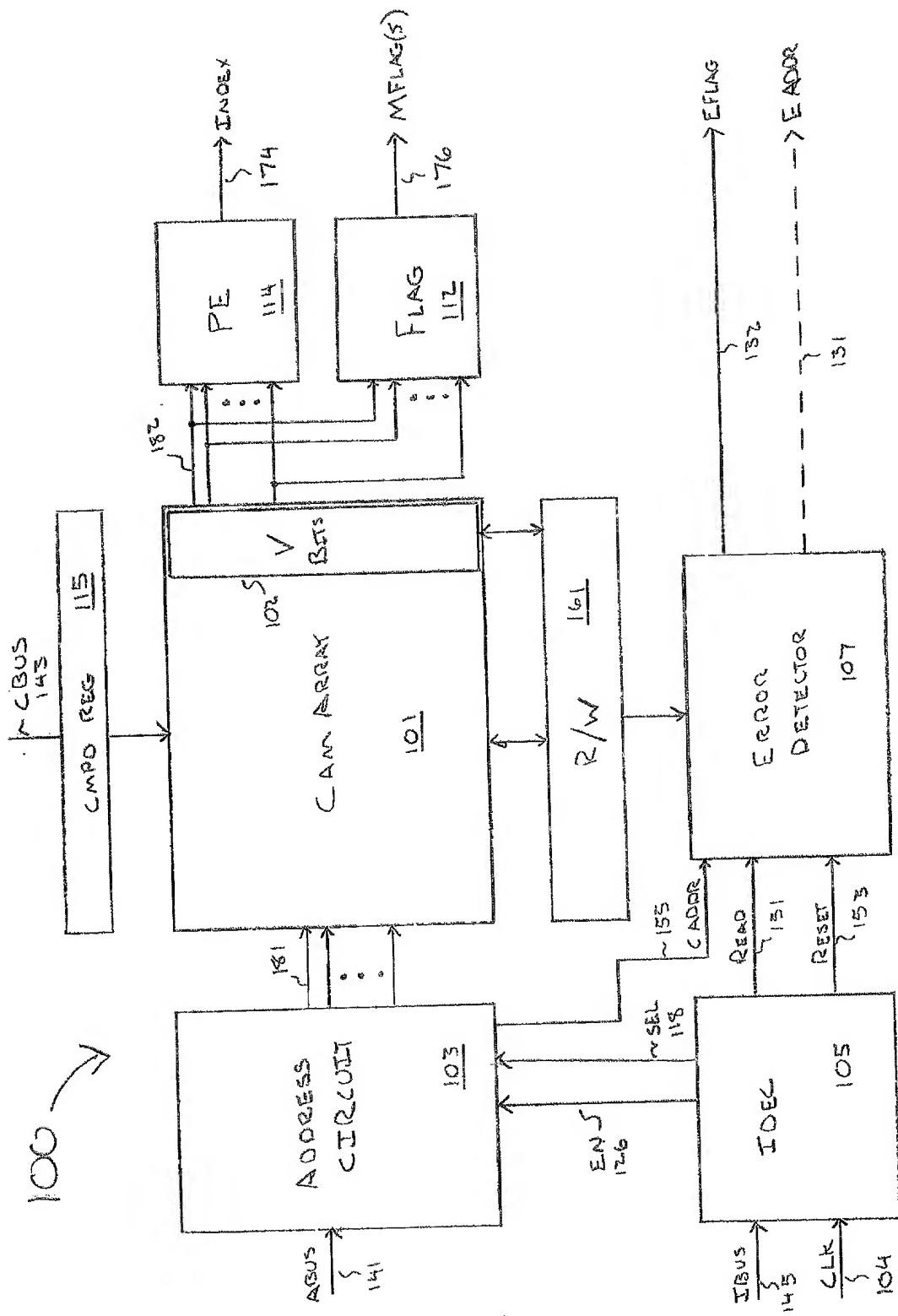


FIG. 1



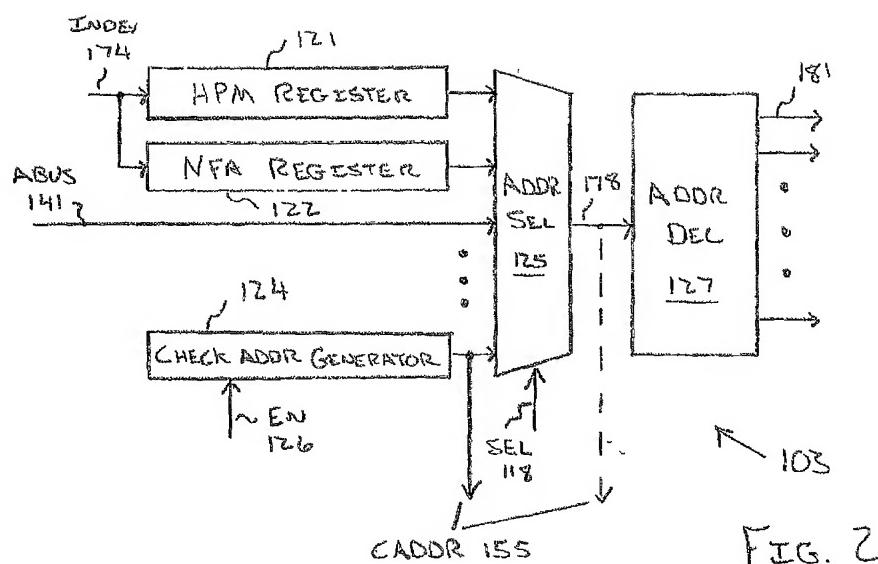


FIG. 2

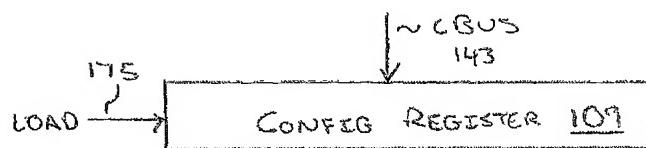


FIG. 3

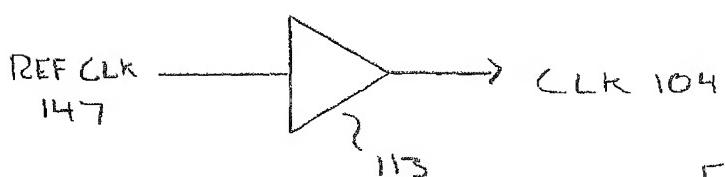
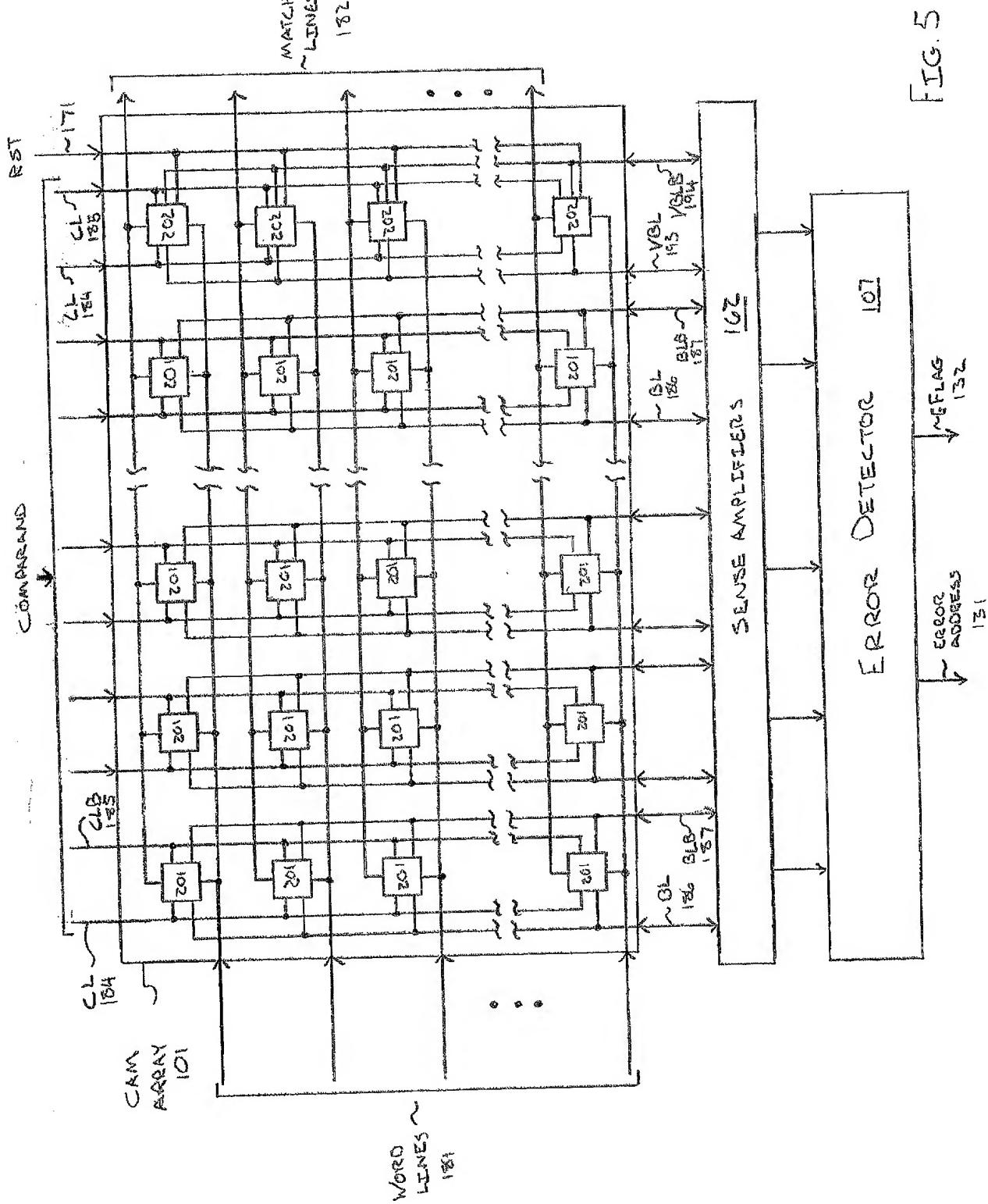


FIG. 4



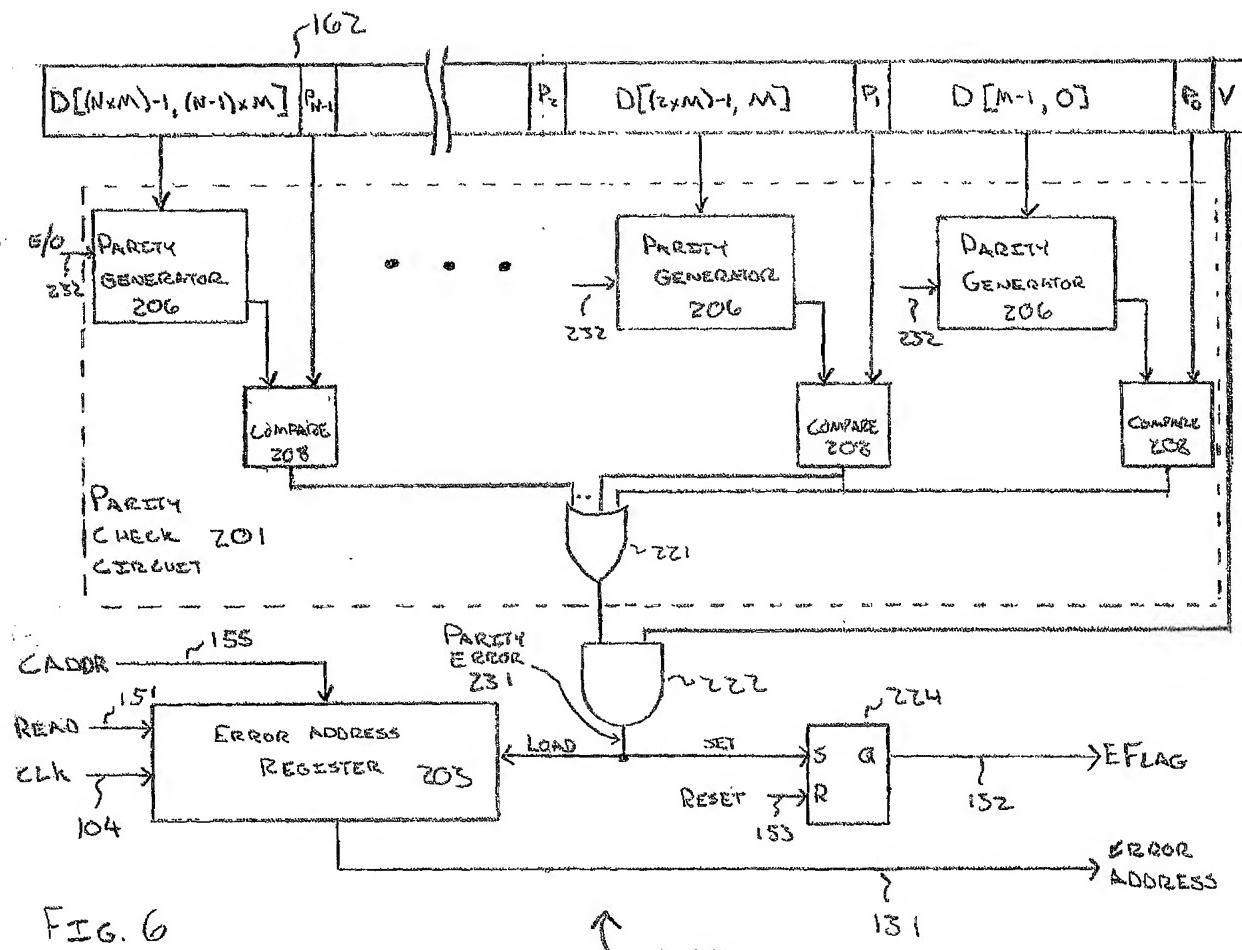


FIG. 6

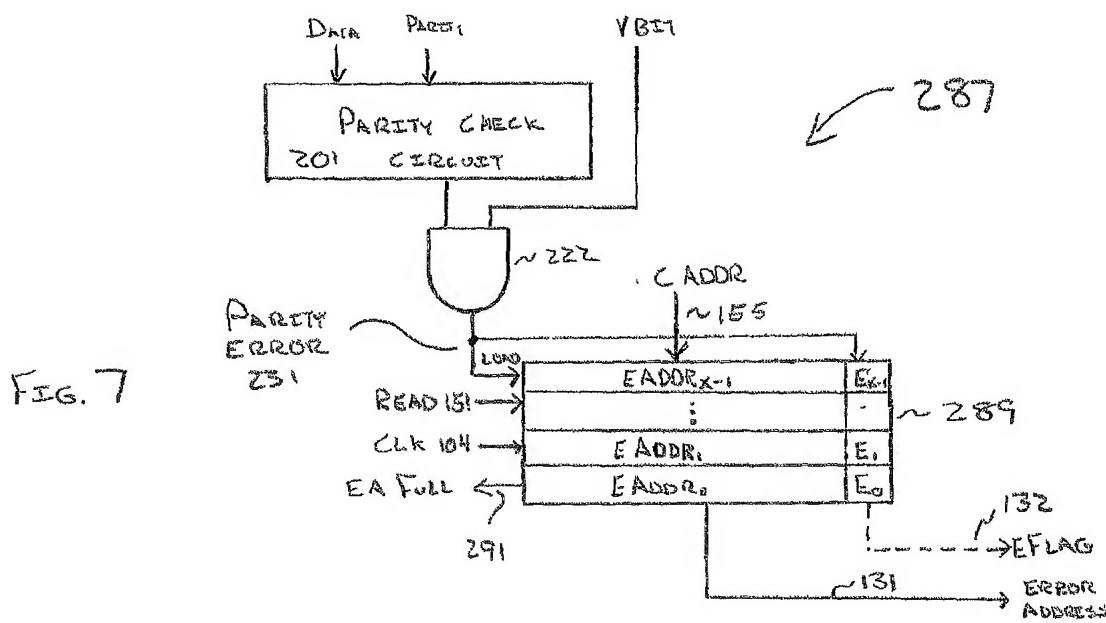


FIG. 7

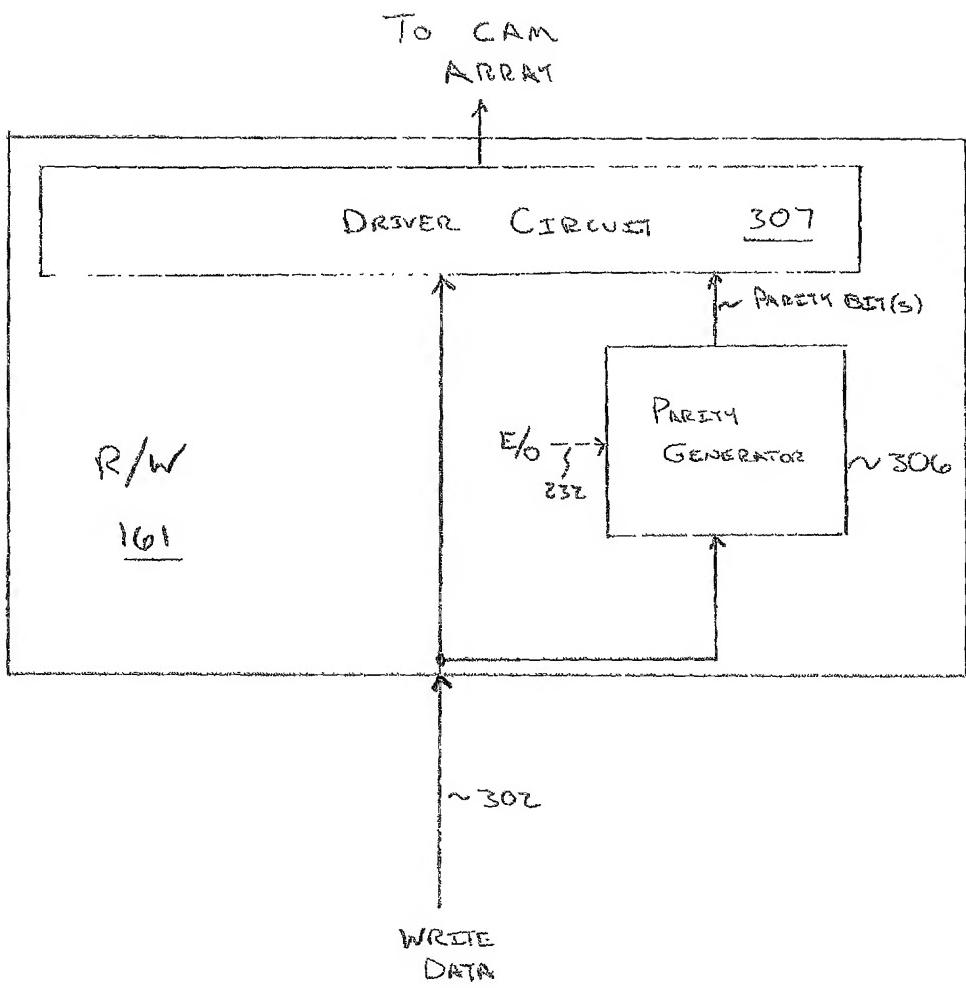


FIG. 8

FIG. 9

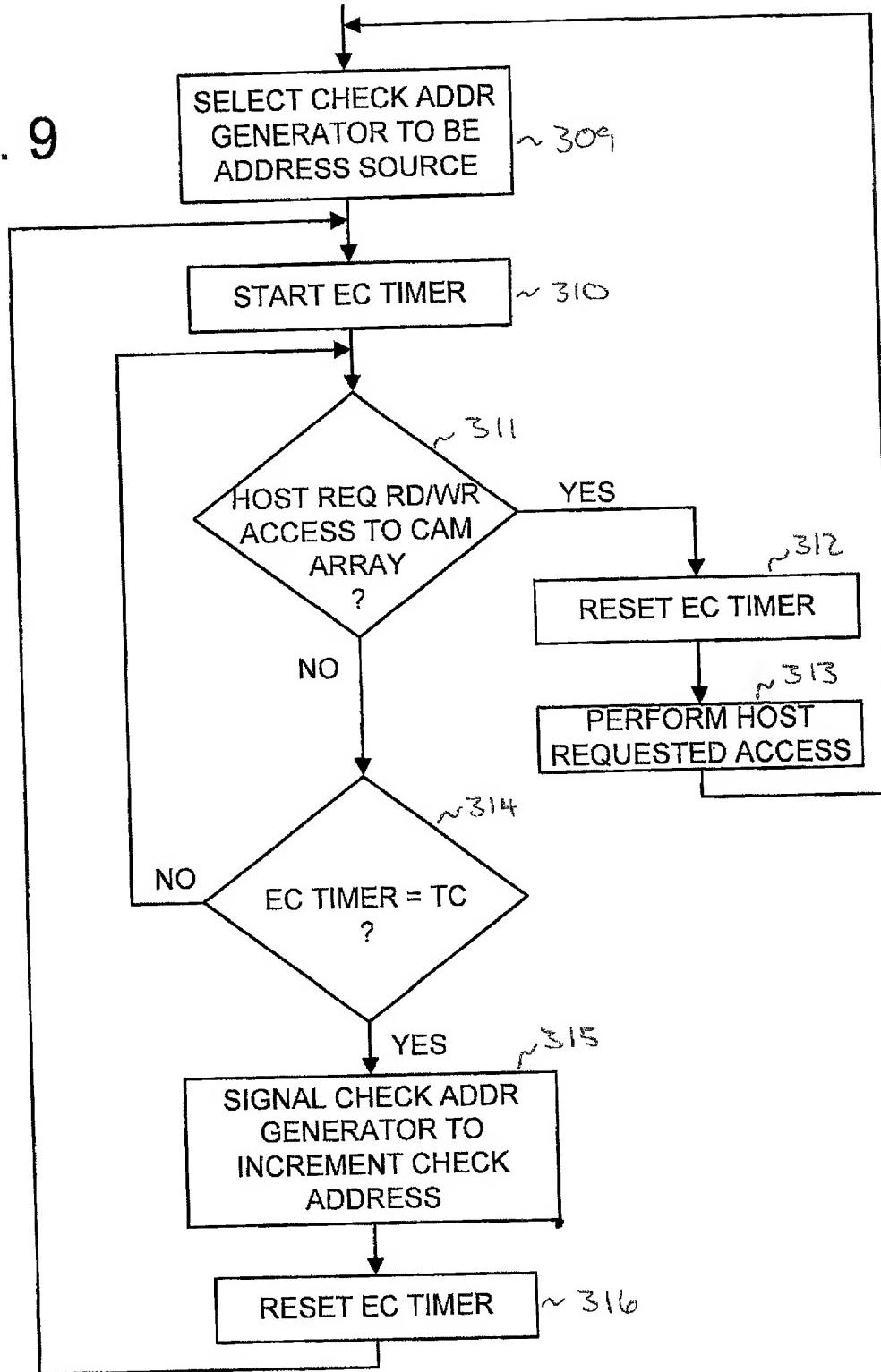


FIG. 10

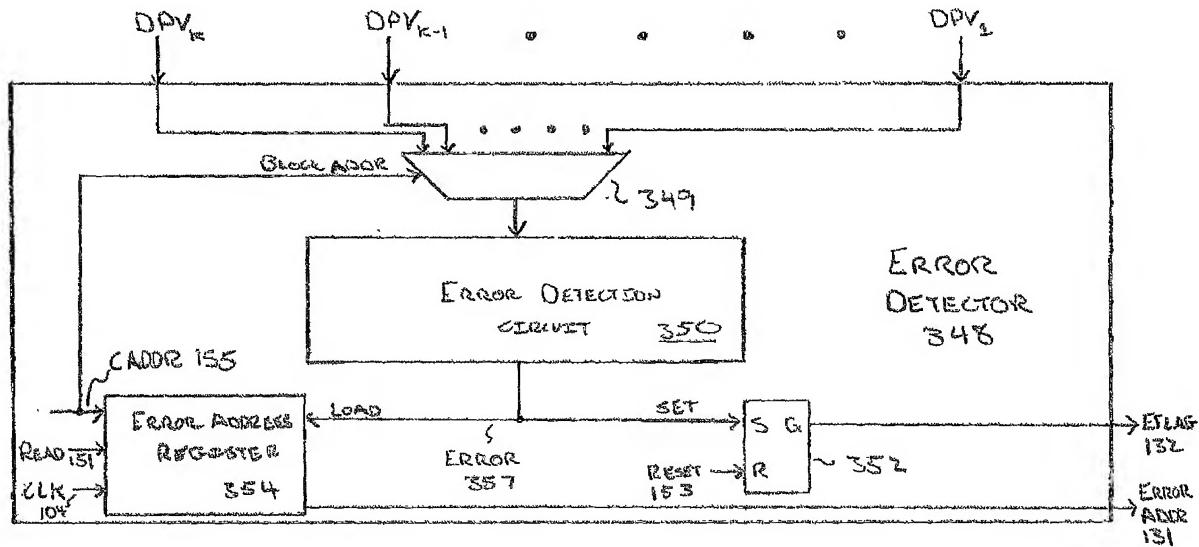
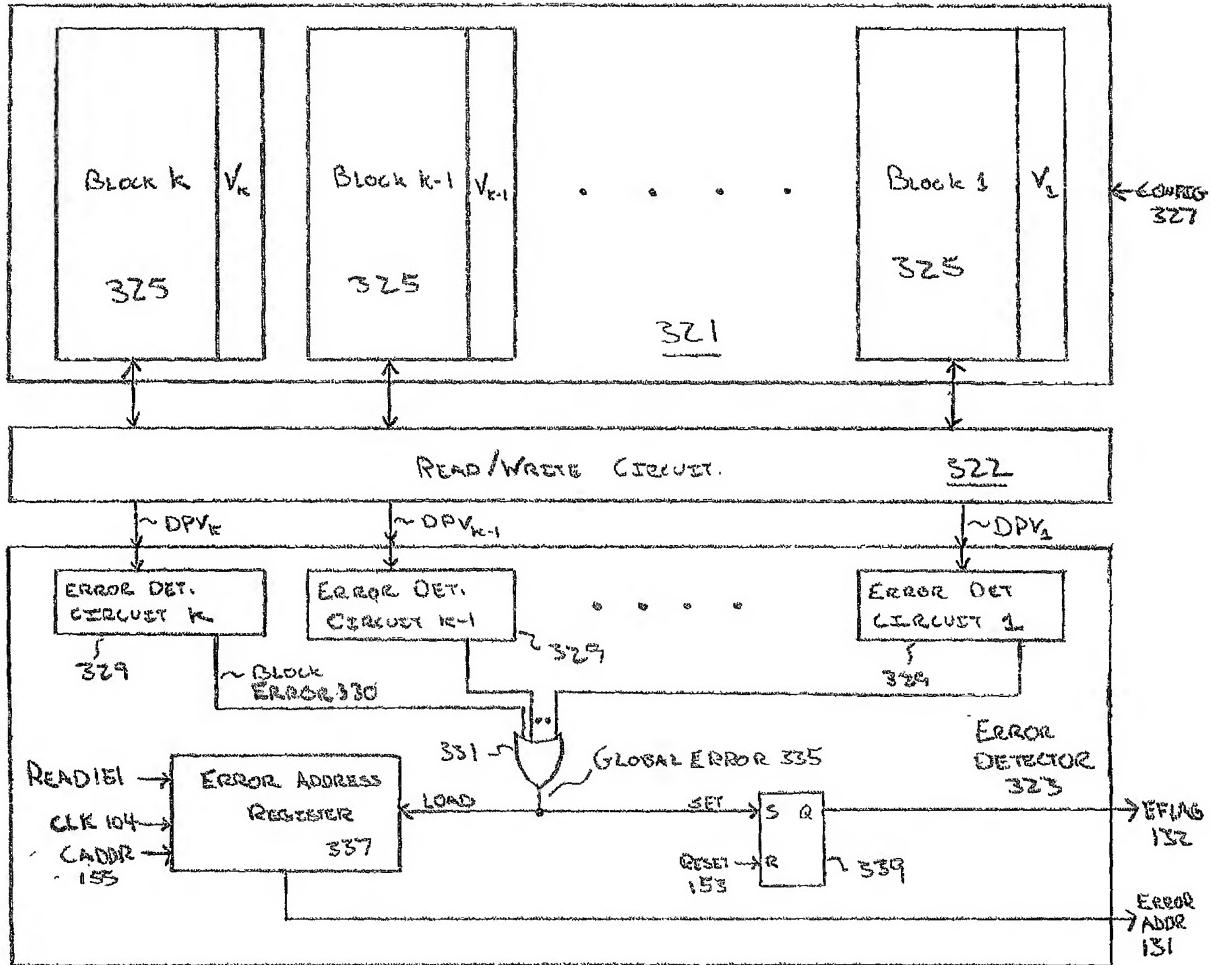
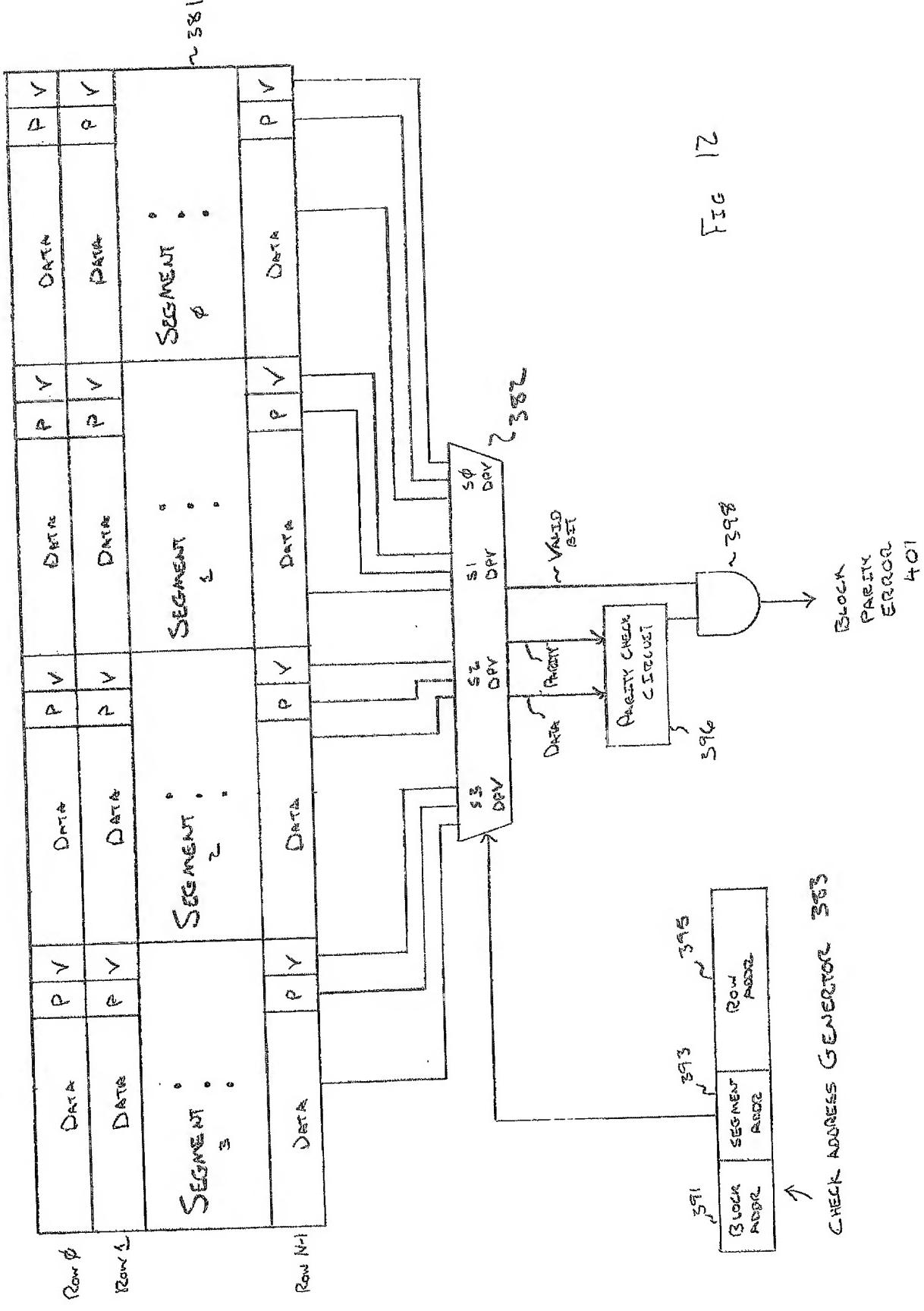


FIG. 11



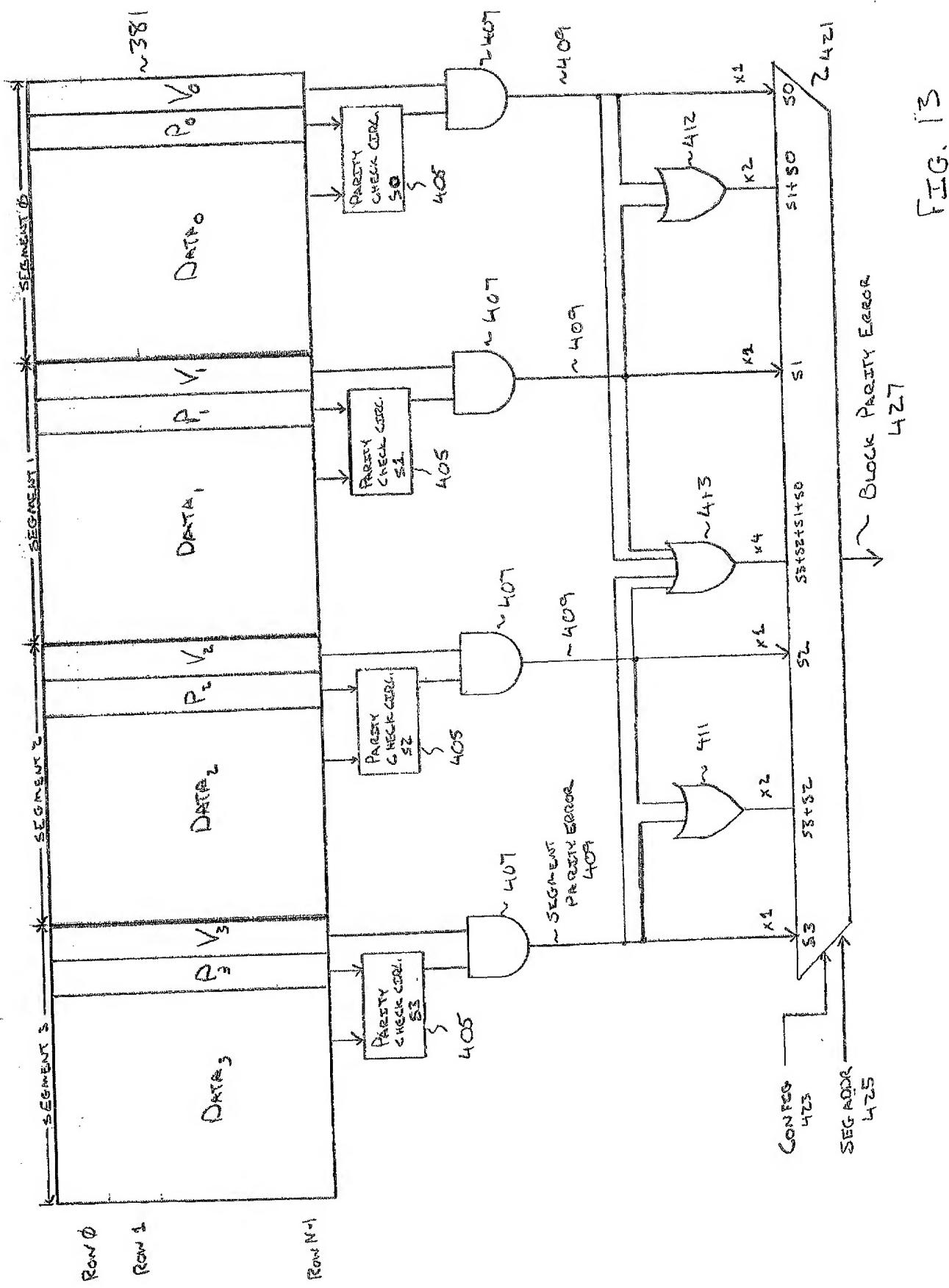
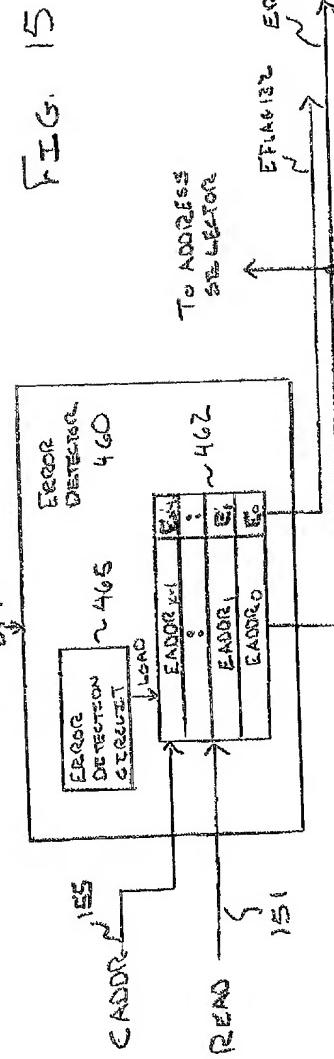
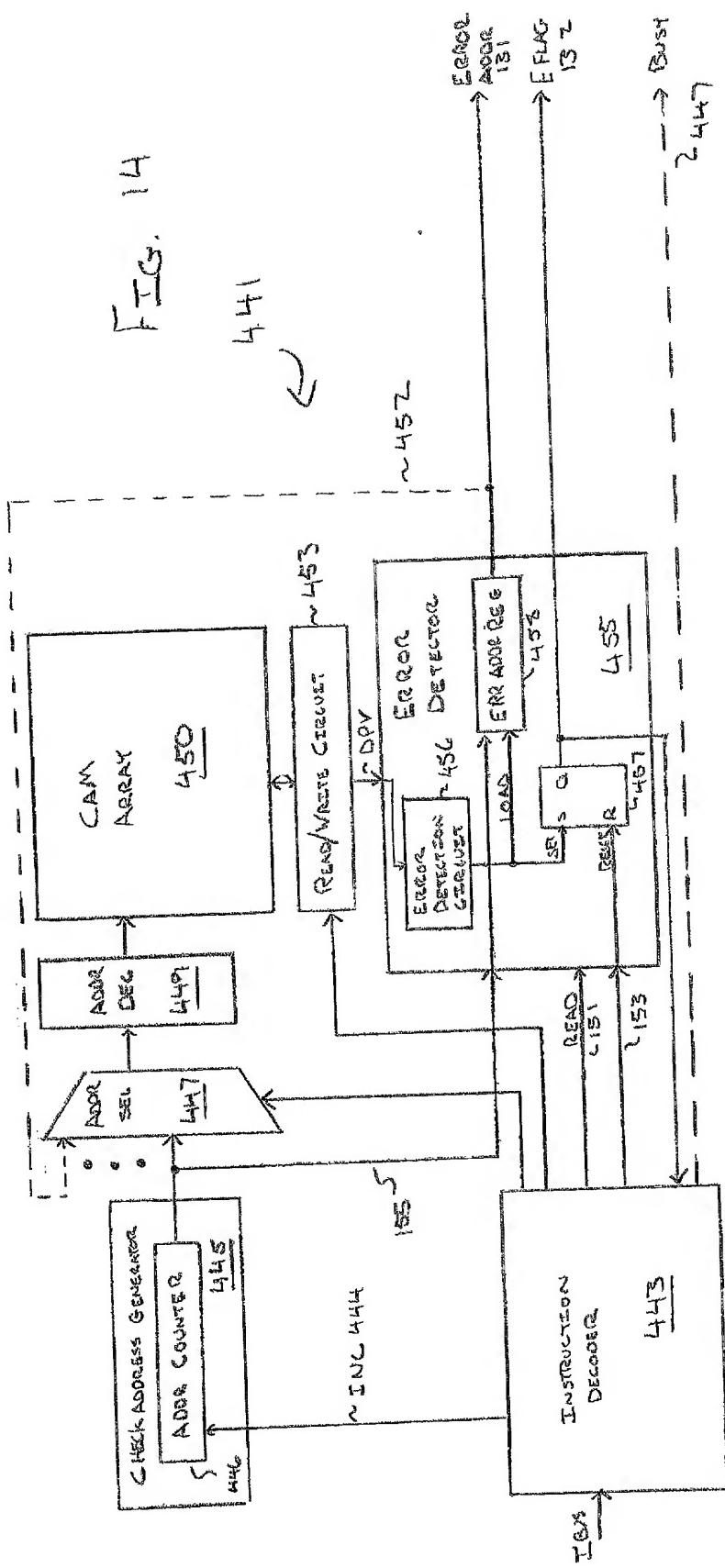


FIG. 13

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~ Block Parity Error



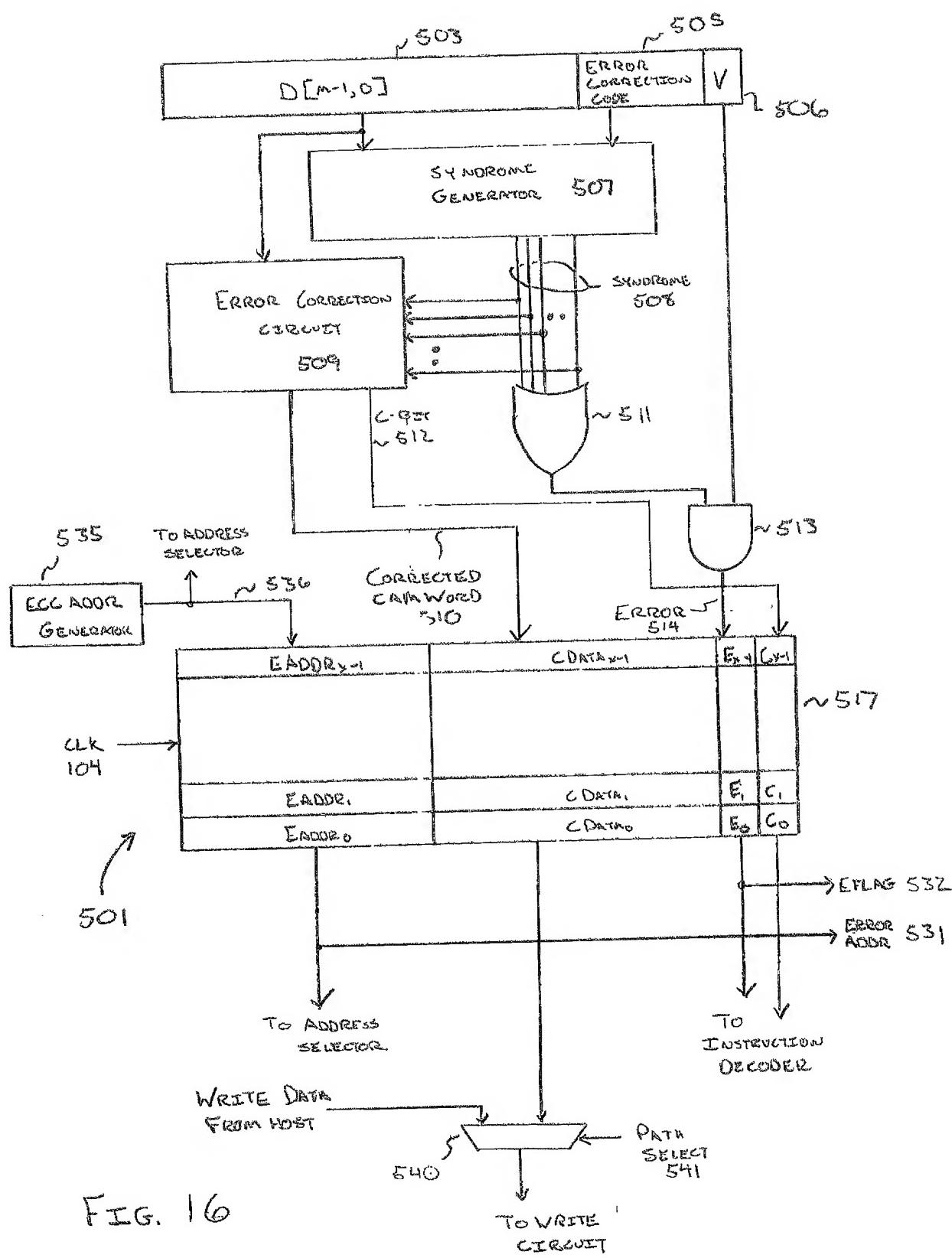


FIG. 17

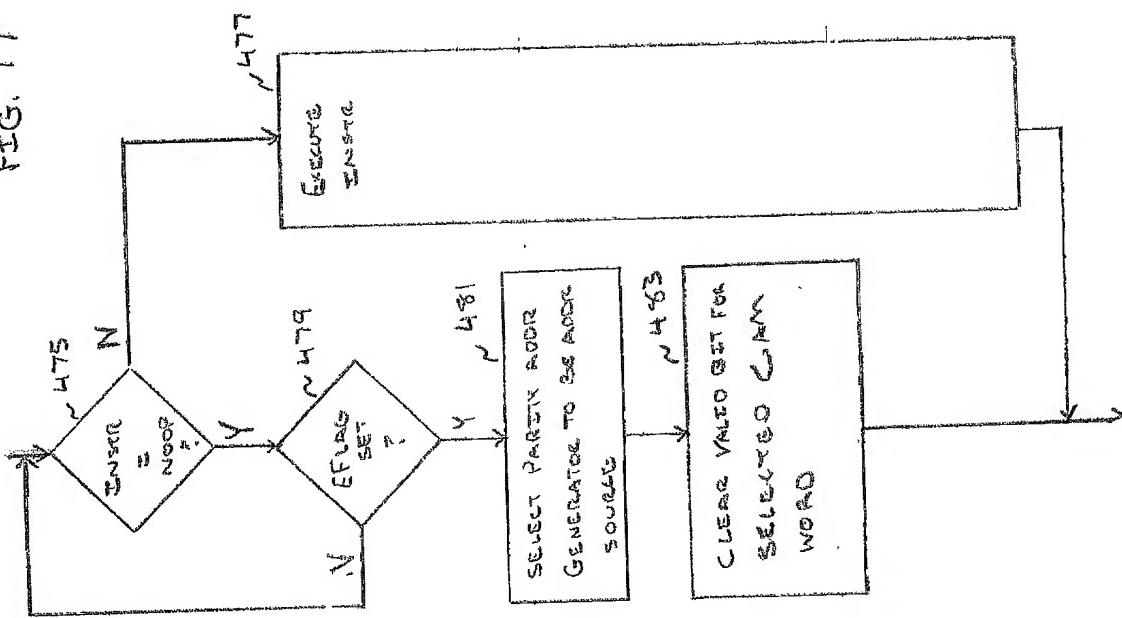
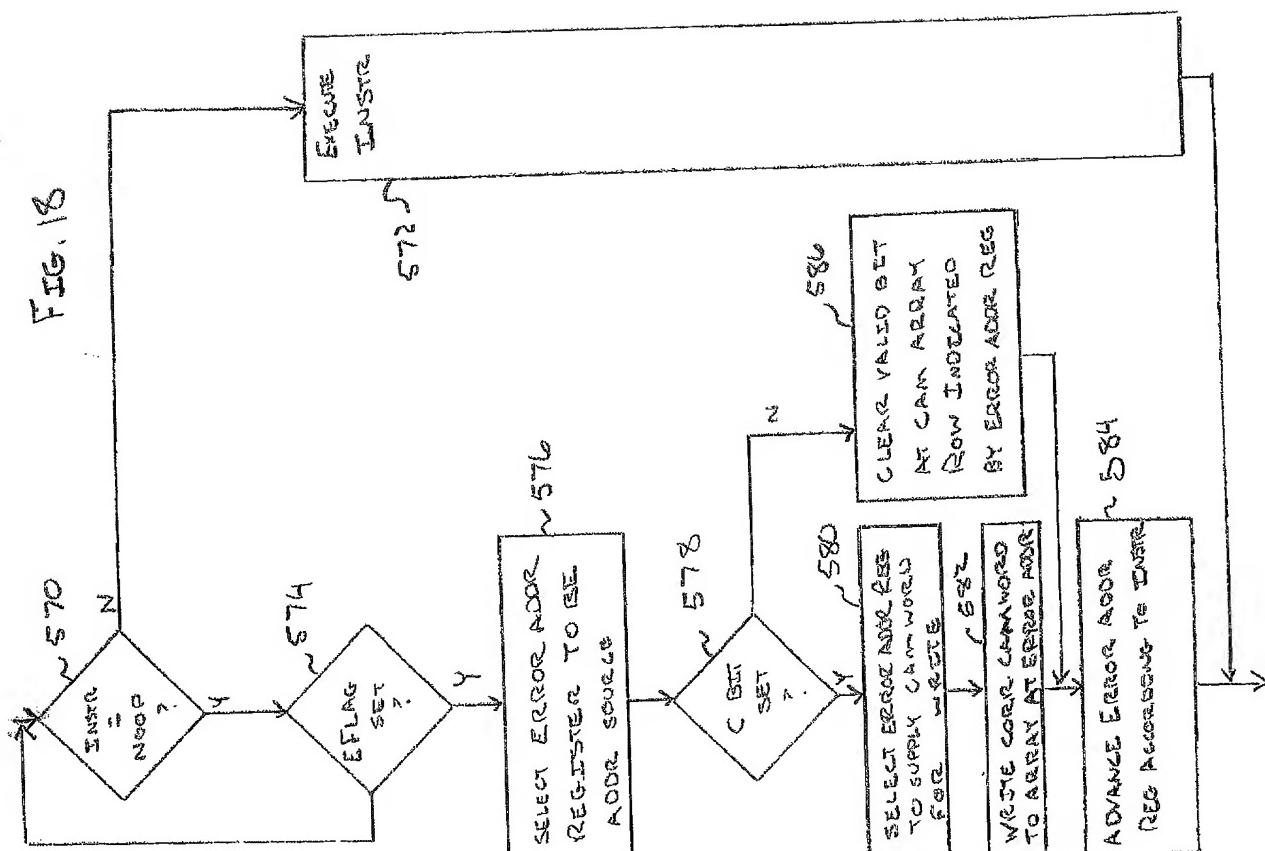


FIG. 18



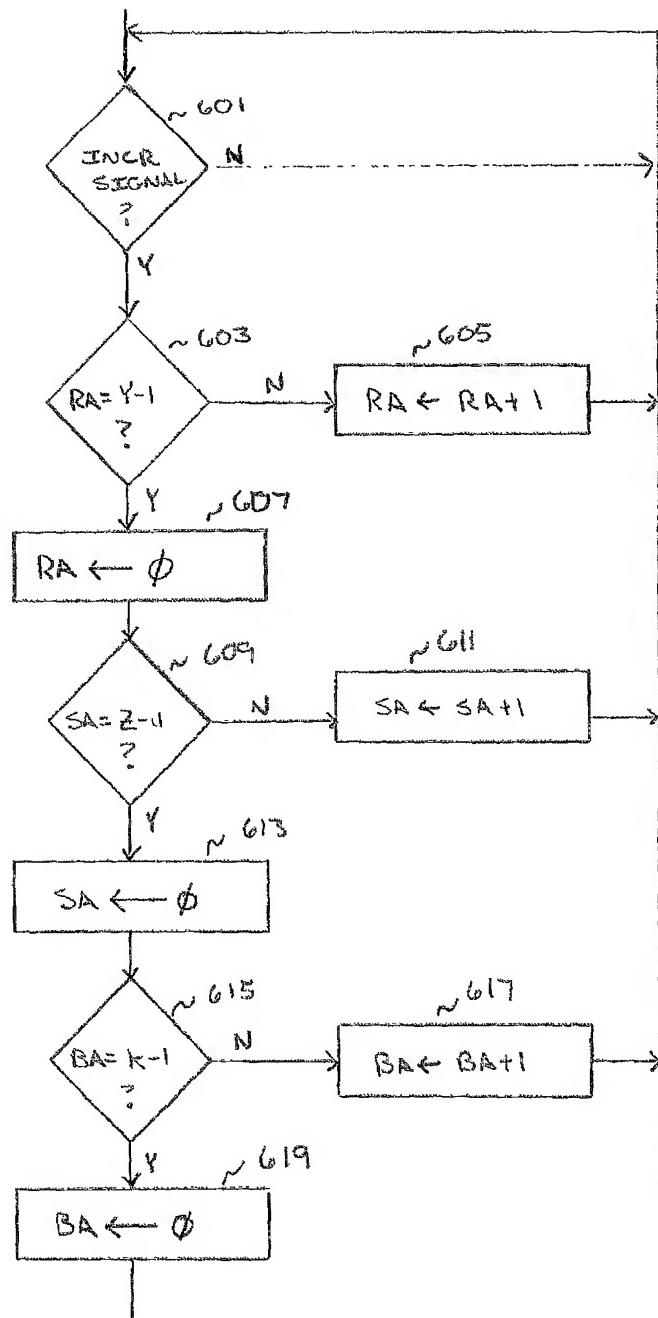
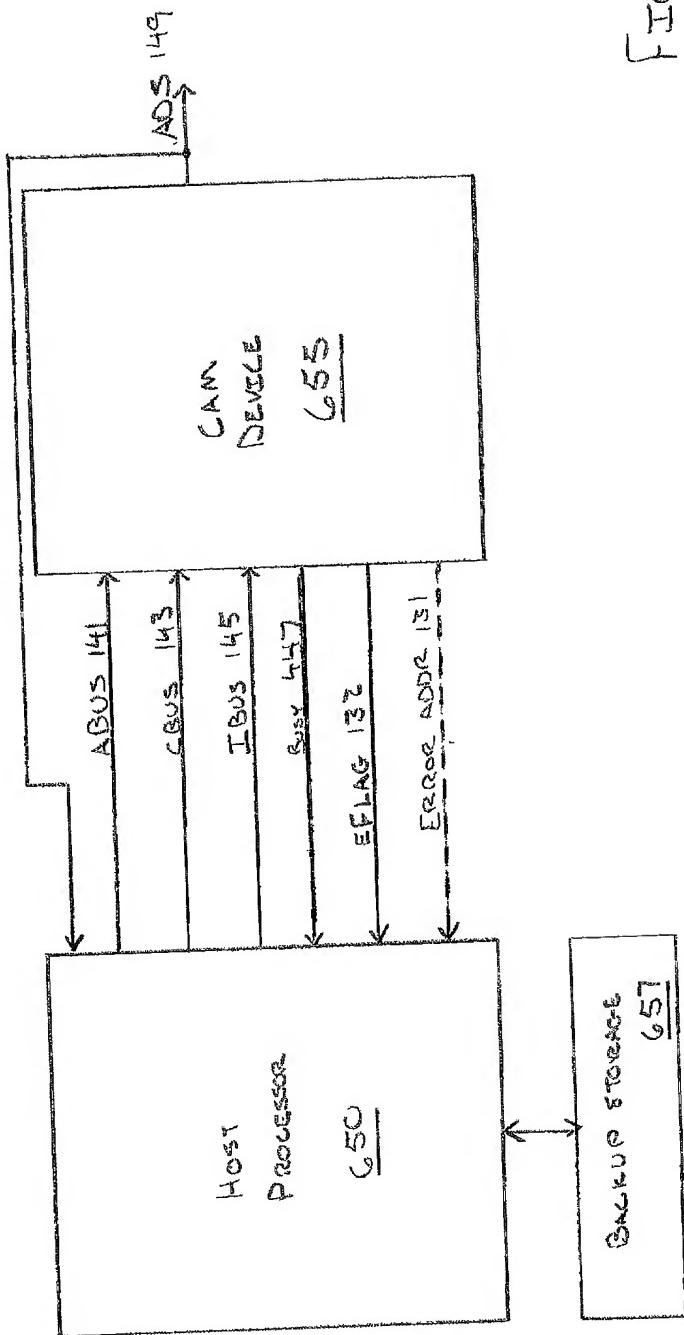


FIG. 19

FIG. 20

↙ C51



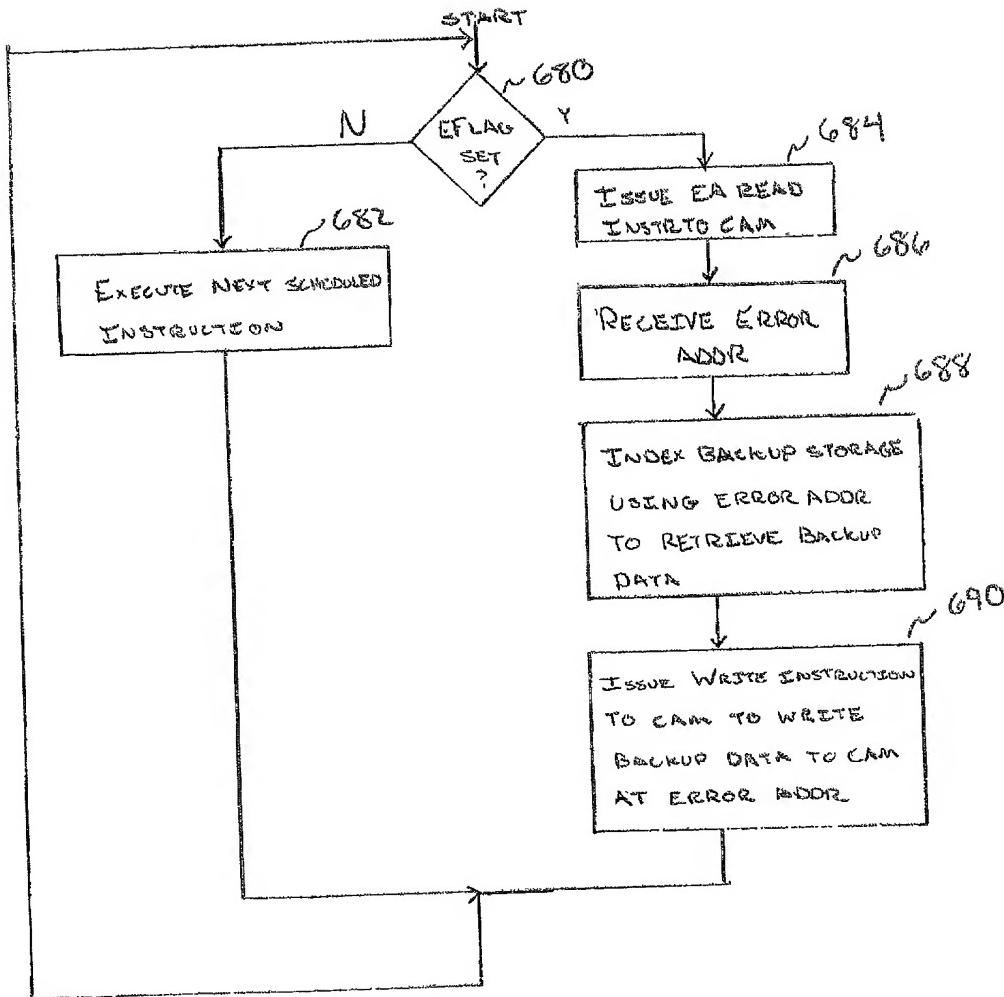


FIG. Z1

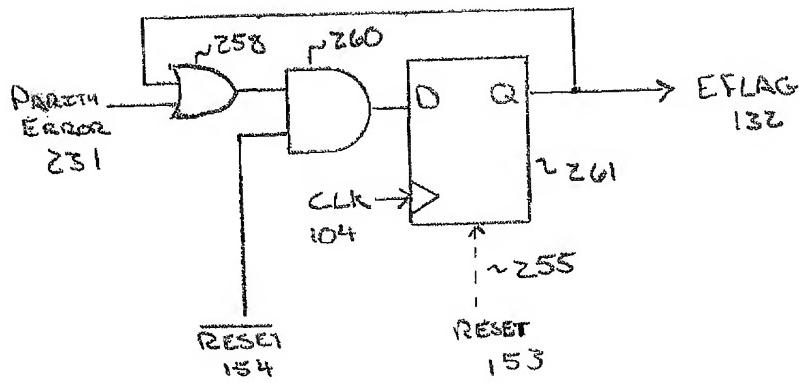


FIG. Z2

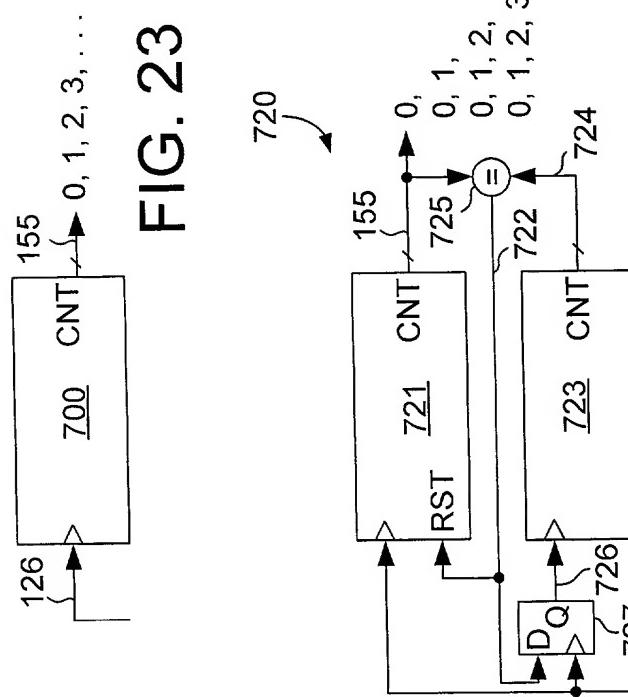


FIG. 23

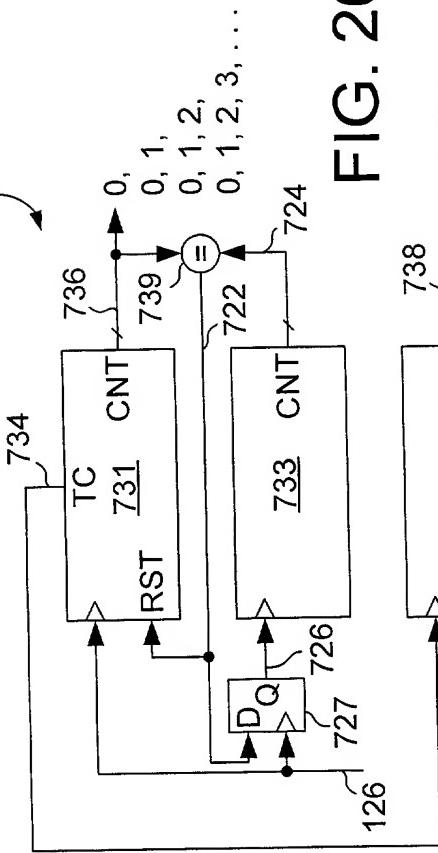


FIG. 26

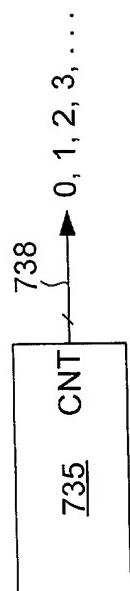


FIG. 24

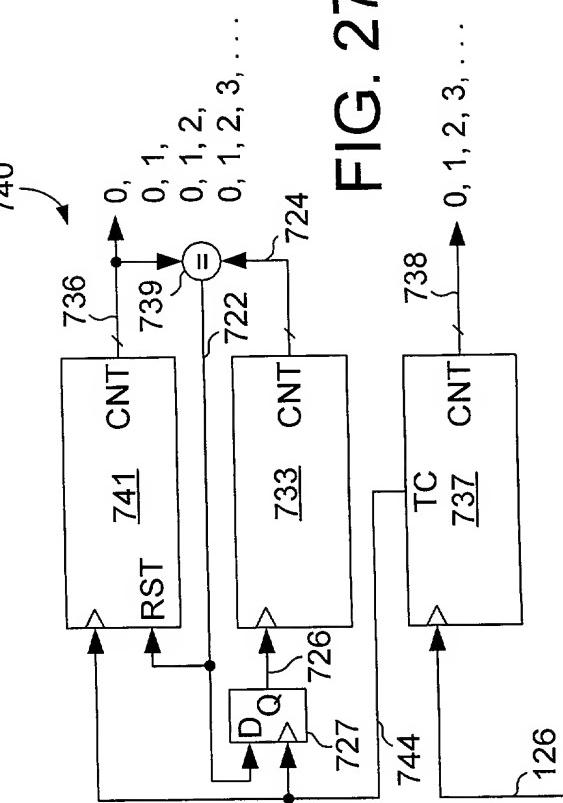


FIG. 27

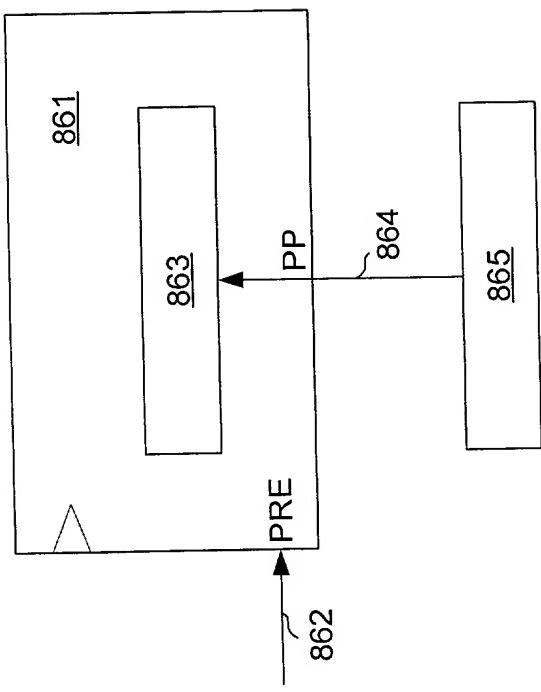


FIG. 29

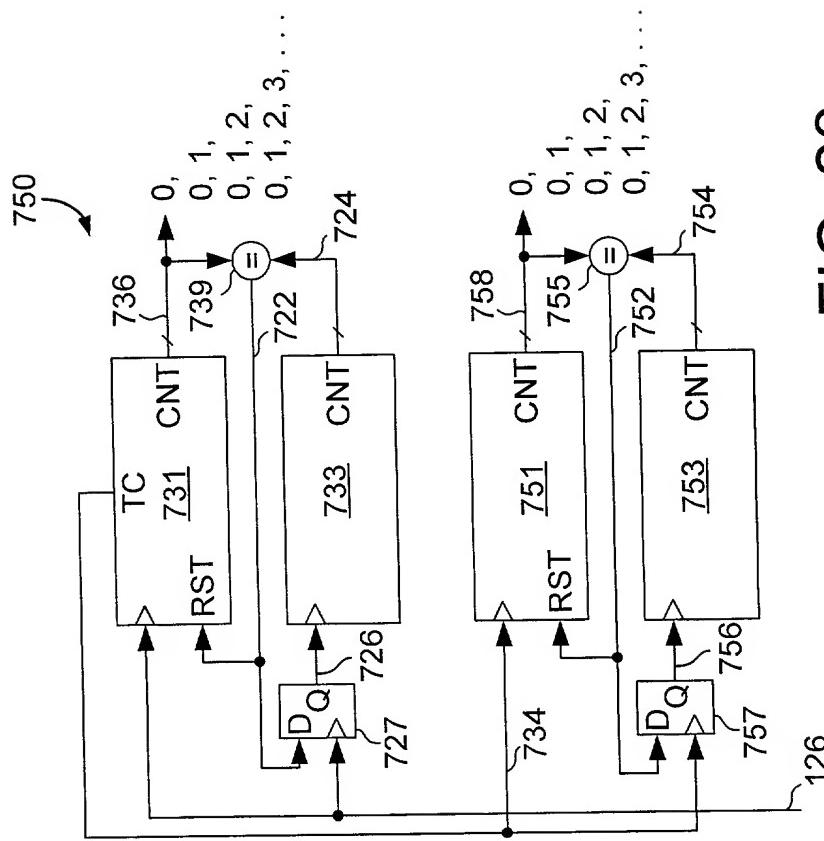


FIG. 28